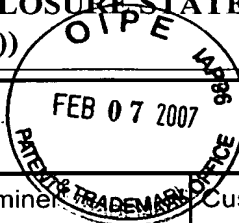


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(d))

Docket No.
200-0664

In Re Application Of: Joseph G. Walacavage et al.



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
09/965,905	September 28, 2001	J. Proctor	33481	2123	4248

Title: **METHOD OF PART FLOW MODEL FOR
PROGRAMMABLE LOGIC CONTROLLER LOGICAL
VERIFICATION SYSTEM**

Address to:

Commissioner for Patents

The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(c), and on or before payment of the issue fee, and is accompanied by the Statement as specified in 37 CFR 1.97(e) and the fee set forth in 37 CFR 1.17(p).

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposits Account No. 06-1510 as described below.
- ☒ Charge the amount of \$180.00
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.
- ☐ Payment by credit card. Form PTO-2038 is attached.

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Certificate of Transmission by Facsimile*

I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax no. _____) on _____ (Date) _____ Signature _____ Typed or Printed Name of Person Signing Certificate

Certificate of Mailing by First Class Mail

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on <u>February 5, 2007</u> (Date) _____ Signature of Person Mailing Correspondence Daniel H. Bliss _____ Typed or Printed Name of Person Mailing Certificate

*This certificate may only be used if paying by deposit account.

Signature
Daniel H. Bliss (Reg. No. 32,398) [0693.00242]
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Record I.D. 81082348

Dated: February 5, 2007

02/07/2007 HVUJONG1 00000013 061510 09965905

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**STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

Docket No.
200-0664

In Re Application Of: Joseph G. Walacavage et al.

FEB 07 2007

Application No. 09/965,905	Filing Date September 28, 2001	Examiner J. Proctor	Customer No. 33481	Group Art Unit 2123	Confirmation No. 4248
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Invention: **METHOD OF PART FLOW MODEL FOR
PROGRAMMABLE LOGIC CONTROLLER LOGICAL
VERIFICATION SYSTEM**

COMMISSIONER FOR PATENTS:

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

Check applicable statement herebelow:

Statement Under 37 CFR 1.97(e)(1)

- ☐ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Statement Under 37 CFR 1.97(e)(2)

- ☒ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.


Signature

Dated: February 5, 2007

Daniel H. Bliss (Reg. No. 32,398) [0693.00242]
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February 5, 2007

(Date)

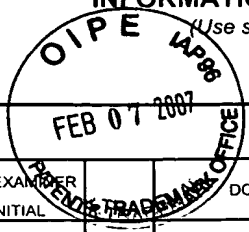

Signature of Person Mailing Correspondence

Daniel H. Bliss

Typed or Printed Name of Person Mailing Correspondence

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



ATTY DOCKET NO.

200-0664

APPLICATION NO.

09/965,905

APPLICANT(S)

Joseph G. Walacavage et al.

FILING DATE

September 28, 2001

GROUP ART UNIT

2123

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,050,088	09/17/1991	Buckler et al.			
	5,758,123	05/26/1998	Sano et al.			
	6,185,469	02/06/2001	Lewis et al.			
	6,292,707	09/18/2001	Hair et al.			
	6,292,715	09/18/2001	Rongo			

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	EP 0 508 308 A3	04/02/1992	EPO				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Beck et al., "Applying a Component-Based Software Architecture to Robotic Workcell Applications". IEEE Transactions on Robotics and Automation, Vol. 16, No. 3, June 2000.
	Frey et al. "Formal Methods in PLC Programming", IEEE International Conference on Systems, Man and Cybernetics, Nashville, Tennessee, October 2000.
	Leduc, Ryan James, "PLC Implementation of DES Supervisor for a Manufacturing Testbed: An Implementation Perspective", Thesis submittal, Graduate Department of Computer and Electrical Engineering, University of Toronto, 1996, pages i-xv, 50-51, 129-143, obtained via http://www.cas.mcmaster.ca/~leduc/rleduc-masc.pdf .

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFORMATION DISCLOSURE CITATION
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200-0664

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			S. Vedapudi, "Using MCM to do Emulation of a Car Assembly Line", Brooks Automation Symposium, 2001, pages 1-4.
			W. Dong, F. Palmquist, and S. Lidholm, "A Simple and Effective Emulation Tool Interface Development for Tricept Application", Proceedings of the 33rd ISR, October 7-11, 2002, 4 pages.

EXAMINER

DATE CONSIDERED

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